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Attorney's Docket No.: 10559-391001  
Client's Ref. No.: P10256 - ADI APD1812-1-US

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**OFFICIAL FAX NO: (571) 273-8300**

Number of pages including this page 19

Applicant : Ryo Inoue et al. Art Unit : 2183  
Serial No. : 09/745,104 Examiner : Tonia L. Meonske  
Filed : December 20, 2000 Assignee: Intel Corporation and Analog Devices, Inc.

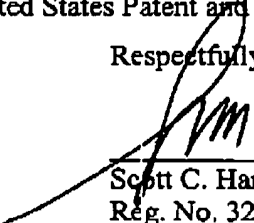
Title : Hardware Loops and A Pipeline System Using Advanced Generation of Loop  
Parameters (As Amended)

**Mail Stop AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet are an Amendment and Transmittal  
Letter, faxed this 20<sup>th</sup> day of July, 2005, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: July 20, 2005

  
\_\_\_\_\_  
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ryo Inoue et al. Art Unit: 2183  
Serial No.: 09/745,104 Examiner: Tonia L. Meonske  
Filed: December 20, 2000 Assignee: Intel Corporation and Analog  
Devices, Inc.  
Title: HARDWARE LOOPS AND A PIPELINE SYSTEM USING ADVANCED GENERATION OF  
LOOP PARAMETERS (AS AMENDED)

**Mail Stop AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL LETTER

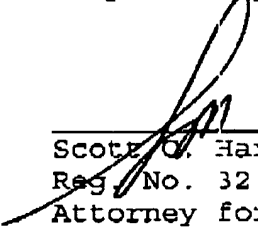
Correspondence relating to this application is enclosed.  
The required fees are computed below. Please apply any charges  
not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	29	-	29	=	0	\$0
Independent	6	-	6	=	0	\$0

TOTAL FEE DUE \$0

Respectfully submitted,

Date: 7/20/05

  
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## CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby certify that this correspondence is  
being transmitted by facsimile to the Patent and  
Trademark Office on the date indicated below.

July 20, 2005

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Veronica Whalen

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Certificate

